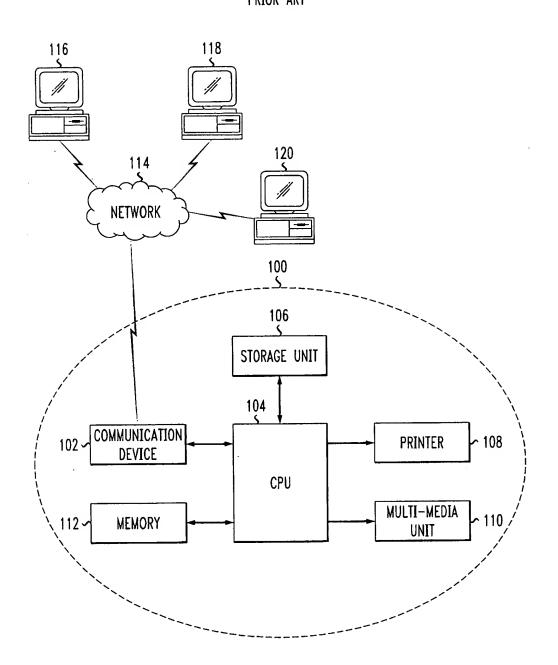
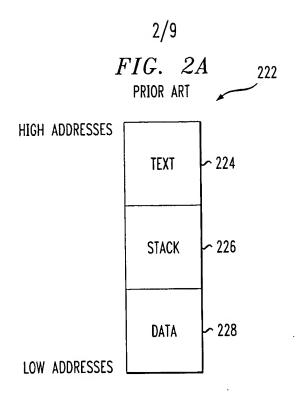
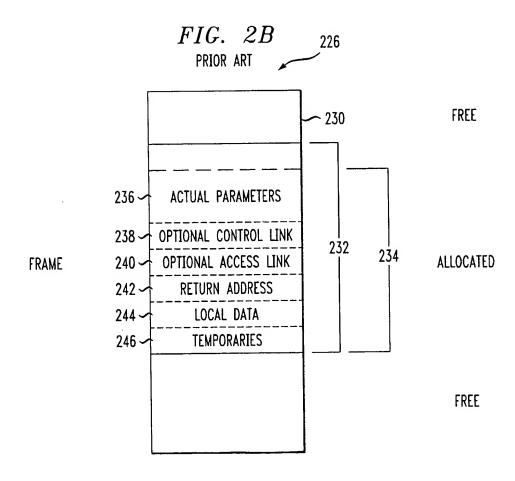
1/9

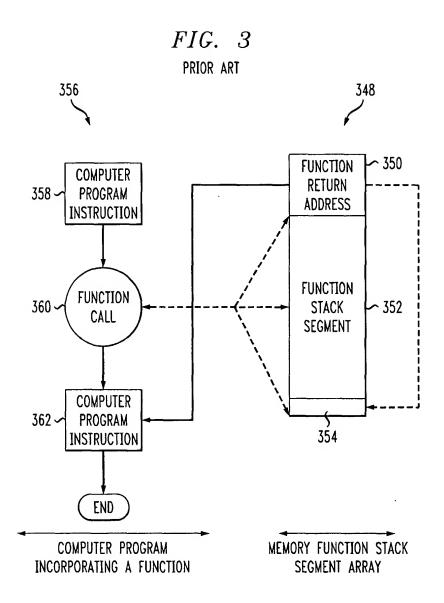
FIG. 1
PRIOR ART

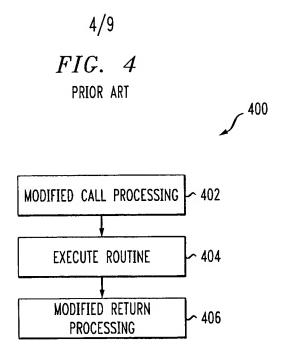


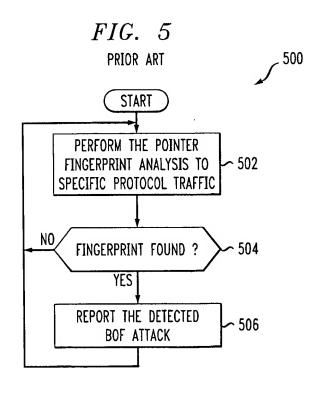




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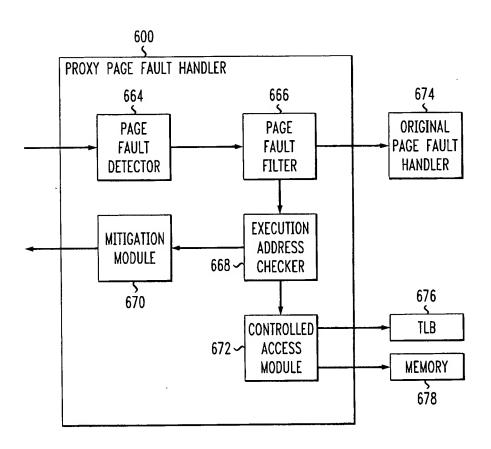




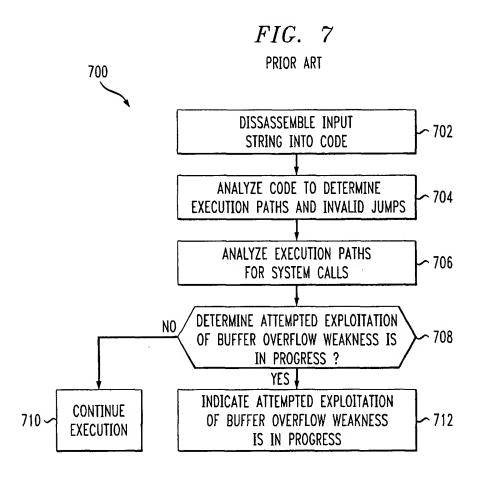


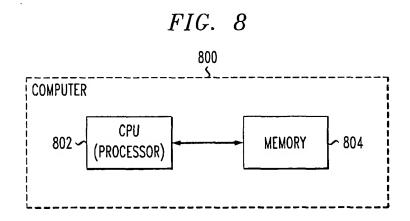
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FIG. 6



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7/9 FIG. 9 912 - 906 LOAD IMAGE 916 - 908 **EXECUTABLE EXECUTION IMAGE** ANALYZER - 910 T00L DATA IMAGE **PROCESSOR** 914 **EXECUTION** HISTORY IMAGE - 918 ALERTS AND MESSAGES

